

### APPLICATION NOTE

# SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

APN-104, Edition 1



SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

# SIPROTEC Application

# SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

APN-104, Edition 1

### Content

| 1   | SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps     | . 3 |
|-----|---|-----|
| 1.1 | Introduction  | . 3 |
| 1.2 | Glossary of Terms and Abbreviations                                     | . 4 |
| 1.3 | Low Power Instrument Transformer basics                                 | 5   |
| 1.4 | SIPROTEC 5 7SY82 - new components and considerations                    | 16  |
| 1.5 | 7SY82 Universal protection basics relevant to testing and commissioning | 19  |
| 1.6 | Tipps & Tricks  | 40  |
| 1.7 | References  | 43  |
| 1.8 | Conclusion  | 43  |

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

# **1** SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

### 1.1 Introduction

The constant change in distribution networks, the presence of new loads, new agents in the market, distributed generation, renewable energy sources, new regulations, digitalization of substations, drives the need for DSOs to use new technologies to increase their sustainability, flexibility, reduction of carbon footprint, cost reduction and safety for workers. One mean to reach these goals are the so-called Low Power Instrument Transformers (LPIT) who are beginning to have a greater relevance.

These LPITs, which are regulated under the standard IEC 61869, have several operating principles and technical data (Nameplate) that are not necessarily managed by the commissioning personnel. Additionally, the use of the tools provided for secondary injections require additional data which is not used in tests with conventional technology today.

This document provides an overview of the LPIT technology covered by the standard as well on secondary test methods and test tools applicable for Universal device **7SY82** protection and **LPIT** technology. Examples with different **LPIT** manufacturers and the necessary considerations for the performance of overcurrent tests will be shown.

As this document covers the test for the new Universal SIPROTEC 5 7SY82 and the module IO141 (at the time when document has been released) we only focus on the LPIT for medium voltage switchgears with technology covered in the standard IEC 61869 -10 and 11, in this context is not covered LPIT technology as current transformers that works whit Hall or optical principles (i.e. Faraday Effect).

Target persons:

- Engineers and technical experts working in Technical Sales department who are defining LPIT solutions.
- Engineers and technical experts who are configuring & commissioning **7SY82** Universal protection device.

Required skills:

- SIPROTEC 5 DIGSI 5
- Omicron Test Universe

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

### **1.2 Glossary of Terms and Abbreviations**

| BCU                   | Bay Control Unit  |
|-----------------------|---|
| BI                    | Binary Input  |
| BO                    | Binary Output   |
| Derivative LPCT [1]   | Low-power passive current transformer providing an output signal proportional to the derivate   |
|                       | of the input signal, LPCT based on non-magnetic-core coil technology without a built-in         |
|                       | integrator (e.g., Rogowski coils) are derivate LPCT.  |
| FAT                   | Factory Acceptance Test   |
| IED                   | Intelligent Electronic Device   |
| IT                    | Instrument Transformer  |
| LPCT                  | Low Power Current Transformer   |
| LPIT                  | Low Power Instrument Transformer  |
| LPVT                  | Low Power Voltage Transformer   |
| MU                    | Merging Unit  |
| Proportional LPCT [1] | low-power passive current transformer providing an output signal proportional to the input      |
|                       | signal, LPCT based on iron-core technology with a built-in primary converter providing output   |
|                       | voltage are proportional LPTC.  |
| Voltage Divider [2]   | Device comprising resistors, inductors, capacitors (or a combination of these components) such  |
|                       | that, between two points of the device, a desired fraction of the voltage applied to the device |
|                       | as whole can be obtained.   |

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

#### **1.3** Low Power Instrument Transformer basics.

This section provides a technical background about the **Lower Power Instrument Transformer**, including a description of the most common operating principles that are included within the standard.

#### 1.3.1 Technical background – LPIT Technology

The Low Power Instrument Transformer or also called LPIT are current and/or voltage measurement devices which do not provide a significant output power, for example  $\geq$  2.5 VA and it is one important change besides the conventional technology. This document covers the LPIT described in the Standard IEC 61869 Parts -6, -10 & -11.

| PRODUCT FAMIL                                | Y STANDARDS  |                            | PRODUCTS  |                        |
|--|--|----------------------------|---|------------------------|
|  |  | PRODUCT<br>STANDARD<br>IEC |   | OLD<br>STANDARD<br>IEC |
|  |  | 61869-2                    | ADDITIONAL REQUIREMENTS FOR<br>CURRENT TRANSFORMERS   | 60044-1<br>60044-6     |
|  |  | 61869-3                    | ADDITIONAL REQUIREMENTS FOR<br>INDUCTIVE VOLTAGE<br>TRANSFORMER   | 60044-2                |
|  |  | 61869-4                    | ADDITIONAL REQUIREMENTS FOR<br>COMBINED TRANSFORMERS  | 60044-3                |
| IEC 61869-1                                  |  | 61869-5                    | ADDITIONAL REQUIREMENTS FOR<br>CAPACITOR VOLTAGE<br>TRANSFORMERS  | 60044-5                |
| GENERAL<br>REQUIREMENTS<br>FOR<br>INSTRUMENT | IEC 61869-6<br>ADDITIONAL<br>GENERAL<br>REQUIREMENTS | 61869-7                    | ADDITIONAL REQUIREMENTS FOR<br>ELECTRONIC VOLTAGE<br>TRANSFORMERS   | 60044-7                |
| TRANSFORMERS                                 | FOR LOW-<br>POWER<br>INSTRUMENT                      | 61869-8                    | ADDITIONAL REQUIREMENTS FOR<br>ELECTRONIC CURRENT<br>TRANSFORMERS   | 60044-8                |
|  | TRANSFORMERS   | 61869-9                    | DIGITAL INTERFACE FOR<br>INSTRUMENT<br>TRANSFORMERS   |                        |
|  |  | 61869-10                   | ADDITIONAL REQUIREMENTS FOR<br>LOW-<br>POWER PASSIVE CURRENT<br>TRANSFORMERS  |                        |
|  |  |                            | ADDITIONAL REQUIREMENTS FOR<br>LOW-<br>POWER PASSIVE VOLTAGE  | 60044-7                |
|  |  | 61869-11                   | TRANSFORMERS<br>ADDITIONAL REQUIREMENTS FOR<br>COMBINED ELECTRONIC<br>INSTRUMENT<br>TRANSFORMER OR COMBINED<br>PASSIVE TRANSFORMERS |                        |
|  |  | 61869-13                   | STAND ALONE MERGING UNIT  |                        |
|  |  | 61869-14                   | ADDITIONAL REQUIREMENTS FOR<br>CURRENT TRANSFORMERS FOR DC<br>APPLICATIONS  |                        |
|  |  | 61869-15                   | ADDITIONAL REQUIREMENTS FOR<br>DC<br>VOLTAGE TRANSFORMERS FOR DC<br>APPLICATIONS  |                        |

Table 1. IEC 61869 Family standard.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

#### **1.3.2** Low Power Passive Current Transformers.

The low power passive current transformers **(LPCT)** are based on passive technology without any active electronic components, **LPCT** can provide an output signal proportional to the primary input. The standard covers two different types:

- Iron core coils, also called LoPo (as a conventional but low power) or proportional LCPT.
- Rogowski coils.
- 1.3.2.1 Iron core coils.

The Standard **IEC 61869-10** defines: "proportional LPCT consist of an inductive current transformer with primary winding, small core and a secondary winding with minimized losses which is connected to a shunt resistor  $R_{sh}$ . This resistor is an integral component of the proportional LPCT and of great importance for the function and stability of the transformer. The voltage across this shunt resistor is the output signal of the proportional LPCT". [1]

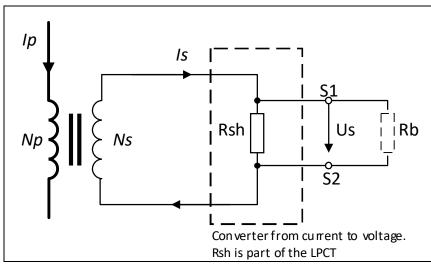


Figure 1. Iron Core coil Current Transformer.

Where:

IP primary current.

R<sub>sh</sub> shunt resistor (converter from current to voltage).

Us secondary voltage.

R₀ burden in ohms.

 $N_{\mbox{\scriptsize P}}$  number of turns in primary winding.

 $N_{\mbox{\scriptsize s}}$  number of turns in secondary winding.

P1, P2 primary terminals.

S1, S2 secondary terminals.

Some benefits of Iron Coil Current Transformers include:

- Current converted into proportional voltage (not additional integration).
- Output signal proportional to the Primary current (not need of derivative elements).
- Output can directly be connected to measurement equipment.
- No temperature drifts.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

• Less energy consumption compared to conventional measurement.

The following Figure shows some of the components for an Iron Core.

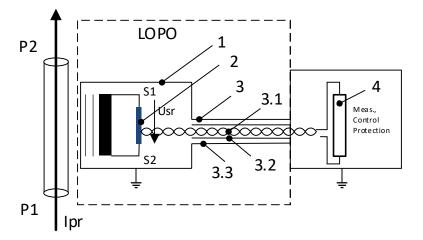


Figure 2. Iron Core (LoPo) Components [3].

#### Where:

- 1. Metal Housing.
- 2. High stability shunt.
- 3. Double shielded cable.
- 3.1 Twisted cable pair.
- 3.2 Internal screen.
- 3.3 External screen.
- 4. Input circuit impedance (burden impedance).

#### 1.3.2.2 Rogowski coils.

The Standard **IEC 61869-10** defines the Rogowski coils principle of operation as: "... operate on the same magnetic field principles as conventional iron-core current transformers (CTs). The main difference between Rogowski coils and CTs is that Rogowski coil windings are wound over a non-magnetic core (air-core), instead of over an iron core. As a result, Rogowski coils are linear since the air-core cannot saturate. However, the mutual coupling between the primary conductor and the secondary winding in Rogowski coils is much smaller than in CTs, resulting in small output power. Therefore, Rogowski coils cannot drive currents through the low-resistance burden as CTs are able to do. Rogowski coil output signals are strong enough to drive microprocessor-based devices that have a high input resistance, and they practically measure voltage across the Rogowski coil's secondary output terminals". [1]

The conventional Rogowski coils consist of a wire wound on a non-magnetic core; the U<sub>5</sub> voltage at the winding terminals is proportional to the rate of change of the current trough the main conductor.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

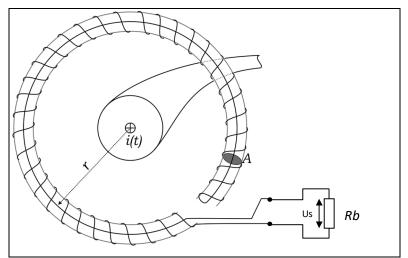


Figure 3. Rogowski coil.

The formula for Us is:

$$U_s = -M \frac{di(t)}{dt}$$

Where M is the mutual inductance.

At the output, the phase angle between the Rogowski primary current and the secondary voltage is close to 90°, due the coil inductance Ls and resistance Rs of the equivalent circuit.

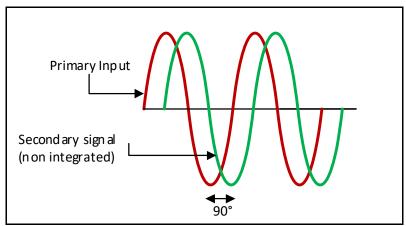


Figure 4. Rogowski coil - Input and output signals.

To measure a signal proportional to the primary current flow the Rogowski coil requires an integration of the output voltage by the measuring device (**7SY82 LPIT** Inputs fulfil this requirement).

Some benefits of Rogowski coils include:

- No danger of opening the secondary winding
- Linear response over wide primary current range No saturation
- Very small size compared to conventional current transformers.
- Smaller ecological footprint
- Less energy consumption

Considerations for protection usage between the two technologies covered by standard:

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| ltem                     | Rogowski Coil                      | Iron core coil   |
|--------------------------|------------------------------------|--|
| Frequency range          | Up to 1 MHz                        | Up to 150 kHz  |
| Saturation               | No                                 | Yes  |
| Nominal primary current  | Wide range of values possible      | Specific transfer ratio must be chosen (i.e. 300A/225mV) |
| Signal processing        | Integration required               | Proportional   |
| Positioning requirements | Required                           | Neglectable  |
| Cost                     | Medium (in case of good coil quali | ty) Low  |

Table 2. Comparison between Rogowski and Iron core coil.

#### 1.3.2.3 Design types for LPCT.

Since the introduction of the first Rogowski Coil various construction techniques have been implemented, many of these variants are related to each manufacturer's own electrical and mechanical solutions, for example, some Rogowski Coil designs are intended for portable solutions and others to be permanently installed on the primary conductor. Here are some of the designs that can be found in the market.

• Flexible coil.



Figure 5. Flexible Rogowski Coil with Integrator [4].

Flexible Rogowski Coils are usually delivered together with a device that integrates the coil output signal, are split-core style and can be installed without any need for electrical or mechanical interruption of the current carrying conductor, while also ensuring galvanic insulation. This makes them easy to use. Accuracy of flexible coils is not too high.

• Split core type.

Split-core current transformers are generally based on the same working principle as solid-core transformers, but the magnetic core is made up of two parts that can be separated. However, unlike a solid-core design, split-core transformers can easily be fitted into an existing installation.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps



Figure 6. Rogowski coil with split core type [5]

• Rigid non-Split wound core Rogowski coil.

Rogowski Coils are wound over a non-magnetic core usually having toroidal shape. This core may be made of plastic, epoxy, or other insulating material.



Figure 7. Rigid non split Rogowski coil types.

• Iron core design for earth fault detection.

In case of earth fault in a three-phase network, a current occurs due to the displacement of the neutral point. This current is implemented with a specific ratio in the output of the sensor. Therefore, the system enables to detect fault and short-circuit currents. The following Figure shows an Iron Core Current sensor solution, with a Split core. For this technology the shunt is installed inside the case.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

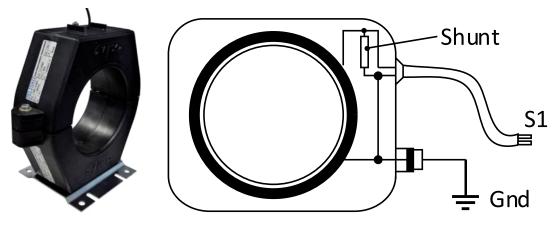


Figure 8. Iron Core Current Transformer Design for Ground Fault Detection [6].

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

#### 1.3.3 Low Power Passive Voltage Transformers

Low-power passive voltage transformers are based on the voltage divider principle. They can be built as resistive dividers, capacitive dividers, or resistive-capacitive dividers. This guideline covers the secondary test using resistive and capacitive dividers as is the LPVT used with **7SY82**. Neither Resistive nor capacitive divider do not use an active primary converter (i.e., without any active electronic component); therefore, there is no need for primary power supply.

The next Figure shows the types of divider principle covered by **IEC61869-11**.

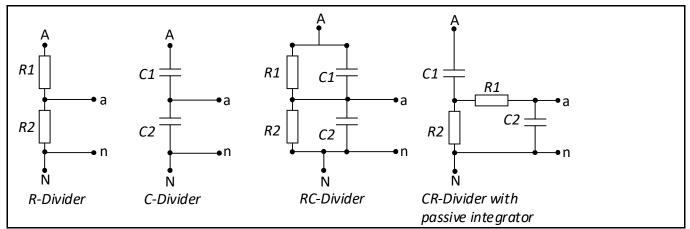


Figure 9. Different divider types covered by IEC 61869-11.

#### 1.3.3.1 R divider

As shown in the Figure 9 the R divider consists of two series Resistance, the drop voltage on the terminal a-n can be calculated by the following formula:

$$V_{a-n} = V_{A-N} \frac{R_2}{R_1 + R_2}$$

Where the Ohmic value of R1 must be larger than R2.

Some benefits of R-Divider include:

- Linear response over whole measurement range.
- Provides non-inductive coupling, No ferro resonance effects.
- No temperature drifts.
- New installations and retrofit possible.
- No danger due secondary voltages.
- Sensor applicable for AIS or GIS.
- Improves operational efficiency.

The components mainly are cast in resin. Against electromagnetic fields the components (active part) can be encapsulated in a metallic housing or shielded with an electrode or screen.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

1.3.3.2 C divider

The C divider consists of two series capacitors, where the impedance formula for a capacitor is given by:

$$X_c = \frac{1}{2\pi fC}$$

The voltage drop in terminal a-n is given by:

$$V_{a-n} = V_{A-N} \frac{X_{C2}}{X_{C1} + X_{C2}}$$

Some benefits of C-Divider include:

- Eliminates primary switching transients and resonance problems.
- Eliminates the space required for VT compartments.
- Improves operational efficiency.
- Frequency range up to 1 MHz.
- Efficient use of resources, Reduce CO2 impact.
- Smaller weight and size of the Switchgear due to smaller dimensions of the sensors.

#### Note:

For improved accuracy of C dividers, a temperature compensation could be implemented. Since the Capacitance is temperature dependent, the accuracy can be influenced with rising temperatures, especially if the sensor is placed closed to conducting material. A PT100 element, contained in the sensor, can be connected to the 7Y82 according to [2] Table 1104, the pin assignment for Temperature sensor are pins 485.

#### 1.3.3.3 Design types for LPVT.

The design of voltage measurement sensors will depend, for example, on the final installation, outside or indoor LPIT as well a difference in the design depending on the insulating mode of the switchgears (AIS or GIS). This section covers the indoor LPIT technology.

For air-insulated switchgears, a recurrent solution of manufacturers is the conventional insulator-type design, as can be seen in Figure 10. Its design allows it to be used as a post insulator but can be used as a stand-alone unit as well.



Figure 10. Voltage divider sensor for AIS as post insulator.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

Other solution for AIS is a bushing-type design, in this case the use of components within the switchgear is optimized, the Figure 11 shows an example of Bushing-type LPIT (a) and the LPIT installed in the busbar compartment of an AIS switchgear (b).

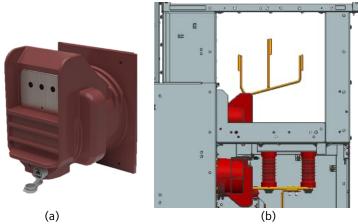


Figure 11. LPIT bushing type for AIS.

For **GIS**, the conventional design is as shown in Figure 12. These insulators are usually mounted on the cable outlet at the back of the T-connector. Therefore, the insulating plug is replaced by the voltage sensor. The cone of the voltage sensor is designed in accordance with EN50181, type C. Due to the standardized design it is possible to equip different T-connectors, as also can be seen in Figure 12.



Figure 12. Voltage divider sensor for GIS Switchgear. [6]

The following Figure shows the internal components of this kind of sensor type, based on a resistive divider.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

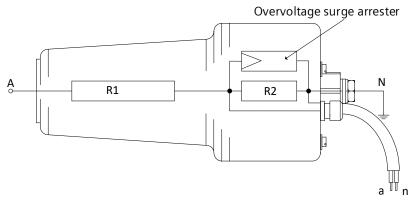


Figure 13. Resistive Divider sensor for GIS solution components. [6]

Finally, it is possible to find combinations of the measurement technologies on the market (also called as combi-sensor), i.e. in the same case it is possible to measure voltage and current by means of sensors. The following figure illustrates some examples of combined LPITs.



Figure 14. Combination Voltage and Current Sensors. [4]

For GIS solution, **SIEMENS** has developed the SIBushing, an outside-cone bushing type C with integrated measurement of current, voltage and temperature. For this technology the sensors used are Capacitive divider and Rogowski coil for voltage and current measurement respectively.



Figure 15. SIBushing for SIEMENS GIS Solution in Medium Voltage.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

### 1.4 SIPROTEC 5 7SY82 - new components and considerations

**7SY82** universal control and protection device is the new IED of the **SIPROTEC 5** family. It contains the **IO141** board with 4 RJ45 inputs for the **LPIT**s (please refer to Hardware Manual for more detailed information). The terminal assigned to the module are identified by the frame in the next Figure.

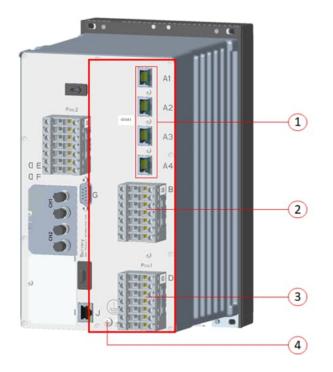


Figure 16. **7SY82** device with IO141 module.

Where:

- (1) LPIT measuring inputs A1-A4
- (2) Binay Inputs terminal B
- (3) Binary inputs and outputs terminal D
- (4) Protective grounding terminals

**LPIT** measuring inputs **A1** to **A4** are divided into groups. The inputs of a group can only be used together. The following table shows the available groups:

| Group 1                   | Group 2                   | Group 3                | Group 4                 |
|---------------------------|---------------------------|------------------------|-------------------------|
| RJ45 socket 1 to 3        | RJ45 socket 1 to 3        | RJ45 socket 4          | RJ45 socket 4           |
| Pins 1 and 2 respectively | Pins 7 and 8 respectively | Pins 1 and 2           | Pins 7 and 8            |
| 3-phase current sensors   | 3-phase voltage sensors   | 1-phase current sensor | 1-phase current/voltage |
|                           |                           |                        | sensor                  |

Table 2. Group Assignment.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

#### Note:

When a secondary test will be performed this assignment takes relevance regarding the secondary injection equipment used. This guideline will show the use of an **OMICRON CMC 430** device and the relevant accessories, please be careful when other tests equipment will be used.

To fulfil with the **IEC61869-10/11** standards, the RJ45 input connectors have the Pinout shown in the following Figure.

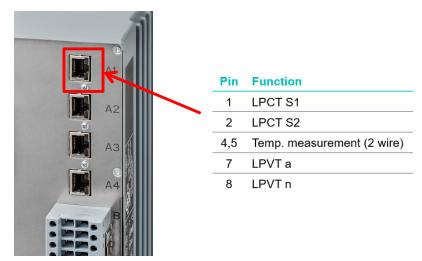


Figure 17. RJ45 connector pin out based on standard IEC61869-10 Table 1003.

#### Note:

The **7SY82** relay is compatible with LPITs that meet the requirements of standards **IEC61869-6**, **10 and 11**.

Because LPIT technology allows for a greater range of use independently of the nominal bay current, it is important to consider the maximum tolerable ranges on the LPCT and LPVT inputs. In the Figure 18 taken from the 7SY82 manual, the measurement range and continuous rating for both currents and voltages is showed. Take an example of a substation with a rated short time withstand current ( $I_k$ ) of 25 kA, the Rated peak withstand current ( $I_p$ ) is 62.5 kA (with a DC time constant of 45 ms and 50 Hz) [7] then, if the solution includes a Rogowski coil with a ratio of 80 A /150 mV, the secondary voltage at the 7SY82 input will be:

$$V_{peak,7SY82\_input} = \frac{I_k}{Ratio LPCT} = \frac{62.5kA}{(80V/150mV)} = 117.2V_{peak}$$

**117.2** V is above the maximum tolerable voltage in the LPCT input  $(35 \text{ V}^*\sqrt{2}) = 49 \text{ V}_{peak}$ 

#### Low-Power Inputs (via IO141 Module)

| All current, voltage, and power dat | a are specified as RMS values.  |  |
|-------------------------------------|---|--|
| Rated frequency f <sub>rated</sub>  | 50 Hz, 60 Hz  |  |
| LPCT input                          | Rated-voltage range   | Measuring range  |
|                                     | V <sub>rated, LPCT</sub><br>For Rogowski coil: 14 mV to<br>565 mV<br>For iron-core coil: 14 mV to<br>515 mV | Protection channel: 0.9 mV to<br>$50 \cdot V_{rated, LPCT}$<br>Measuring channel: 0.9 mV to<br>$1.6 \cdot V_{rated, LPCT}$ |
| LPVT input                          | Rated voltage   | Measuring range  |
|                                     | V <sub>rated, LPVT</sub> : 381 mV to 5 V  | 0.001 $\cdot$ V $_{rated, LPVT}$ to 2.0 $\cdot$ V $_{rated, LPVT}$   |
| Input impedance at 50 Hz / 60 Hz    | 2 MΩ +5 % to -5 % and 50 pF +0 9  | % to -100 %  |
| Continuous voltage rating           | Max. input voltage<br>LPCT: 35 V  |  |
|                                     | LPVT: 10 V  |  |

Figure 18. LPCT and LPVT input ratings.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

Same substation data, but in this case a Rogowski coil with a ratio of 50 V / 22.5 mV, then the secondary voltage at 7SY82 input will be:

$$V_{7SY82\_input} = \frac{I_k}{Ratio LPCT} = \frac{62.5kA}{(50V/22.5mV)} = 28.1V_{peak}$$

For the above example, the specified Rogowski coil is set to substation Rated peak withstand current (62.5 kA) and the LPCT input of the protection relay.

This tolerable range is equally important when developing the protection test, due to the adjustment of maximum currents or voltages that can be injected by the test equipment.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

# 1.5 7SY82 Universal protection basics relevant to testing and commissioning.

This part of the document shows via three cases the configuration settings in **DIGSI**, the considerations that are necessary to consider for **LPIT** of  $3^{rd}$  party and the configuration using an Omicron Test device. Please consider that **DIGSI 5 Version**  $\ge$  **9.60** counts with a data base of different references for an easy configuration.

#### NOTE:

For a detailed setting explanation please refer to the **7SY82** Manual and the document "**Interpretation of technical data** for protection functions Edition 1"

#### 1.5.1 Case 1. Using SIBushing – Integrated Siemens LPIT sensor solution

|                  | SIEN                          | IEN:         | S                                | Siemer           | ns Fa              | actory | y No           | . & | Para        | mete   | ər-F | ile-N          | lo.:  |
|------------------|-------------------------------|--------------|----------------------------------|------------------|--------------------|--------|----------------|-----|-------------|--------|------|----------------|-------|
| Typ              | e: LPIT                       | 2024-0       | 1-18                             | 52732)           | XXX                | X-XX   | -XX            |     |             |        |      | +              | H01   |
| Oper             | ation Temperature             | -50 / +70°C  | >                                |                  |                    | Conta  | ct             |     |             |        |      |                |       |
| Stora            | ge & Transport Temp.          | -50 / +70°C  | ;                                | RJ45-PI          | ug                 | 1      | 2              | 3   | 4           | 5      | 6    | 7              | 8     |
| Rate             | d Frequency (f <sub>r</sub> ) | 50 / 60 Hz   |                                  | LPCT             |                    | S1     | S2             |     |             |        |      |                |       |
| Insul            | ation Class                   | н            |                                  | LPVT             |                    |        |                |     |             |        |      | а              | n     |
| Mass             | Insulation Material           | 0.8 kg       |                                  | Temp.            |                    |        |                |     | Pt100       | Pt100  |      |                |       |
| Prim             | ary Insulation Level          | 36 / 70 / 17 | '0 kV                            | IN MARKED        |                    |        | ase A          |     | Phas        |        |      | Phase          |       |
| Seco             | ndary Insulation level        | 0.82 kV      |                                  |                  |                    |        | or Seri<br>No: | al  | Sensor<br>N | Serial | Se   | ensor S<br>No: | erial |
| Shor             | t Time Current Ith            | 26.3 kA / 3  | S                                |                  |                    |        | 10.            | _   |             | 0.     |      | NO.            |       |
| LPV              | r                             | IEC 61869    | -1/-6/-11                        | <b>EIR-757</b> / | <<br>•             |        |                |     |             |        |      |                |       |
| Upr              | 33 /√3 kV                     | Ratio        | 10.000 / 1                       |                  | CFu                | 1.0    | 4804           |     | 1.04        | 308    |      | 1.036          | 14    |
| FV               | 1.9 x Upr / 8 h               | Class        | 0.5 / 3P                         |                  | φ <sub>0 cor</sub> | 0°     | 10.67'         |     | 0° 10       | 0.46'  |      | 0° 10.         | 33'   |
|                  |                               | Burden       | 2MΩ / 50pF                       |                  |                    |        |                |     |             |        |      |                |       |
| LPC              | т                             | IEC 61869    | -1/-6/-10                        |                  | -                  |        |                |     |             |        |      |                |       |
| Ipr              | 50 A                          | Ratio        | 50 A / 22.5 mV<br>50 A / 27 mV @ | 0                | CFi                | 1.0    | 0476           |     | 1.01        | 326    |      | 1.0224         | 11    |
| K <sub>pcr</sub> | 25                            | Class        | 0.5 / 5P 500                     |                  | φ <sub>0 por</sub> | 0°     | -0.37'         |     | 0° -0       | ).40'  |      | 0° -0.2        | 20'   |
| I <sub>cth</sub> | 1250 A                        | Burden       | 2MΩ / 50pF                       |                  |                    | 2      |                |     |             |        |      |                |       |
| Tem              | perature Sensor - Pt100       | 0 Offset     |                                  |                  | -                  |        |                |     |             |        |      |                |       |
| https            | ://www.siemens.com/LP         | IT           | 100 Ω @ 0°C                      | 1                | CTemp              | 1.1    | <b>90</b> Ω    |     | 1.21        | 0Ω     |      | 1.210          | Ω     |

For Siemens LPIT sensor solution SIBushing the following Figure shows an example of the Nameplate data.

Figure 19. SIBushing Nameplate.

The first step is to configure the LPIT type in DIGSI. This menu is found in the settings of IO141 module (*FG Analog Units - FB LPIT-IO141*). For SIBushing the LPIT technologies used are Rogowski coil and C divider.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| LPIT General        |                         |                     |   |          |  |
|---------------------|-------------------------|---------------------|---|----------|--|
| 1341.3331.20821.100 | Slot number:            | 1                   |   | <b>2</b> |  |
| 1341.3331.20821.101 | LPIT type input 1 to 3: | LPCT: Rogowski coil | • | 🔁 🚲      |  |
| 1341.3331.20821.102 | LPIT type input 4:      | unused              | • | 🔁 🔊      |  |
| 1341.3331.20821.103 | LPIT type input 5 to 7: | LPVT: C divider     | • | 🛃 🔊      |  |
| 1341.3331.20821.104 | LPIT type input 8:      | unused              | • | 📑 🔊      |  |
| 1341.3331.20821.159 | Adaptor and testbox:    | no                  | • |          |  |

Figure 20. LPIT General data in DIGSI for **7SY82** device.

Taking the information contained in the Nameplate, it can be identified that each Nameplate has a Production ID number of the phases, in addition to containing the information of correction factors (*CFu, CFI*) and the angle correction values per phase. In this example the ID number is 52732xxxx-xx-xx, with this ID it is possible to download all the information directly from the database, to do this, it is necessary to follow the following steps.

Click on the indicated icon to download the updated database (JSON file).

| Edit mode: primary            |         | Active: settings group 1 | Change: settings group 1  | Compare: None |     | E 2 🖸 |
|-------------------------------|---------|--------------------------|---------------------------|---------------|-----|-------|
| PIT General                   |         |                          |                           |               |     |       |
| 1341.3331.2082                | 1.100   | Slot num                 | ber: 1                    |               |     |       |
| 1341.3331.2082                | 1.101   | LPIT type input 1 t      | to 3: LPCT: Rogowski coil | •             |     |       |
| 1341.3331.2082                | 1.102   | LPIT type inpu           | ut 4: unused              | •             | 🛃 🔊 |       |
| 134 <mark>1</mark> .3331.2082 | 1.103   | LPIT type input 5 t      | to 7: LPVT: C divider     | •             | 📑 🔊 |       |
| 1341.3331.2082                | 1.104   | LPIT type inpu           | ut 8: unused              | -             | 📑 🔊 |       |
| 1341.3331.2082                | 1 1 5 9 | Adaptor and test         | box: no                   | -             | 🔁 🔊 |       |

Figure 21. Icon for updated database download.

Once the above icon is selected, the menu in Figure 22 will be displayed. At this point the user will have to choose the vendor and the year of production as appropriate. In this case, **SIEMENS SI-MV-GIS**.

| Do you want to update the Vendors list?<br>Vendor of phase sensors: | Select all SIEMENS SI-MV-GIS SIEMENS SI-MV-AIS ABB Zelisko |  |
|---|--|--|
| Select manufacture year option as:                                  | Greenwood-Power Year                                       |  |
| To year.  | 2024   |  |
|   | Download Cancel  |  |

Figure 22. Download sensor data menu.

Once the download has been successful, the following message will be displayed by **DIGSI**.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

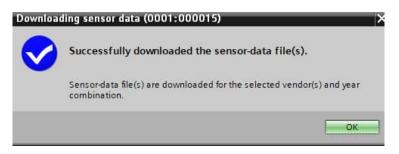


Figure 23. Successful database download.

Now that the database has been downloaded, it is possible to configure the information requested in DIGSI with the **SIBushing** serial number.

First, for current measurement, in the Rogowski coil menu, select SIEMENS SI-MV-GIS in the setting 23101.101 Vendor of *Phase LPIT* and SI Bushing Current Sensor in the Setting 23101.162 in Sensor Type, as shown in Figure 24.

| Vendor data         |                           |                           |
|---------------------|---------------------------|---------------------------|
| 1341.3331.23101.10  | Vendor of phase LPITs:    | SIEMENS SI-MV-GIS         |
| 1341.3331.23101.162 | Sensor type:              | SI Bushing Current Sensor |
| 1341.3331.23101.13  | Select year of manufact.: | yes 💌                     |
| 1341.3331.23101.132 | Year:                     | 2024                      |
| 1341.3331.23101.102 | LPIT Production ID:       |                           |

Figure 24. Vendor data for SIBushing.

In Figure 24 it is possible to observe that once the Sensor type is selected, **DIGSI** will show 3 new settings, in this example the user will be able to indicate if they know the year of production of the sensor with the setting **23101.131** in **Yes** then in the setting **23101.132** indicate the year according to the Nameplate information.

Now, in the setting **23101.102** LPIT Production ID, enter the serial number identified in the Nameplate, in this example (Figure 19) is 52732xxxx-xx-xx.

| Ver | ndor data           |                           |                           |
|-----|---------------------|---------------------------|---------------------------|
|     | 1341.3331.23101.101 | Vendor of phase LPITs :   | SIEMENS SI-MV-GIS         |
|     | 1341.3331.23101.162 | Sensor type:              | SI Bushing Current Sensor |
|     | 1341.3331.23101.131 | Select year of manufact.: | yes 💌                     |
|     | 1341.3331.23101.132 | Year:                     | 2024                      |
|     | 1341.3331.23101.102 | LPIT Production ID:       | 52732                     |

Figure 25. Vendor data for current measurement.

Once the serial is entered, **DIGSI** will search the downloaded database, and if this serial is found within it, the settings will be downloaded and assigned correctly, as can be seen in Figure 26.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| Sens | or data             |                           |             |          |     |
|------|---------------------|---------------------------|-------------|----------|-----|
|      | 1341.3331.23101.103 | Rated primary current:    | 50.0        |          | A   |
|      | 1341.3331.23101.104 | Rated secondary voltage:  | 22.50       |          | mV  |
|      | 1341.3331.23101.111 | Rated phase offset:       | 90.0        |          | ٥   |
|      | 1341.3331.23101.112 | Nominal burden:           | 2 MΩ, 50 pF | <b>*</b> |     |
|      | 1341.3331.23101.105 | Corr. factor for Kr phsA: | 1.0048      |          |     |
|      | 1341.3331.23101.106 | Corr. factor for Kr phsB: | 1.0133      |          |     |
|      | 1341.3331.23101.107 | Corr. factor for Kr phsC: | 1.0224      |          |     |
|      | 1341.3331.23101.108 | Corr. angle phsA:         | -0.4        |          | •   |
|      | 1341.3331.23101.109 | Corr. angle phsB:         | -0.4        |          | •   |
|      | 1341.3331.23101.110 | Corr. angle phsC:         | -0.2        |          | /#. |

Figure 26. Automatic Sensor Data Generation for current measurement.

Data such as Correction Factor Kr per Phase and Correction Angle are automatically assigned, and it is not possible to change the value.

Same process for the Capacitive Divider. By entering the sensor Production ID, DIGSI will show the respective correction factors.

| Vendor data  |  |   |    |
|--|--|---|----|
| 1341.3331.23341.101  | Vendor of phase LPITs:   | SIEMENS SI-MV-GIS   | •  |
| 1341.3331.23341.162  | Sensor type:   | SI Bushing Voltage Sensor   | -  |
| 1341.3331.23341.142  | Select year of manufact.:  | yes   |    |
| 1341.3331.23341.143  | Year:  | 2023  | \$ |
| 1341.3331.23341.102  | LPIT Production ID:  | 52732   |    |
| 1341.3331.23341.103  | Rated primary voltage:   | 10.0  |    |
| 1241 2221 22241 102  | Pated primanucoltage:  | 10.0  |    |
| 1341.3331.23341.104  |  | The second se |    |
| 1341.3331.23341.104  | Rated secondary voltage:   | 1.00  |    |
| 1341.3331.23341.111  | Rated secondary voltage:<br>Rated phase offset:  | 0.00  |    |
|  |  |   |    |
| 1341.3331.23341.111  | Rated phase offset:  | 0.00  |    |
| 1341.3331.23341.111<br>1341.3331.23341.112   | Rated phase offset:<br>Nominal burden:   | 0.00<br>2 MΩ, 50 pF   |    |
| 1341.3331.23341.111<br>1341.3331.23341.112<br>1341.3331.23341.105                        | Rated phase offset:<br>Nominal burden:<br>Corr. factor for Kr phsA:                              | 0.00<br>2 MΩ, 50 pF<br>1.0480   |    |
| 1341.3331.23341.111<br>1341.3331.23341.112<br>1341.3331.23341.105<br>1341.3331.23341.106 | Rated phase offset:<br>Nominal burden:<br>Corr. factor for Kr phsA:<br>Corr. factor for Kr phsB: | 0.00<br>2 MΩ, 50 pF<br>1.0480<br>1.0431   |    |

Figure 27. Automatic Sensor Data Generation for voltage measurement.

1341.3331.23341.110

Corr. angle phsC: 10.8

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

As mentioned in section **1.3.3.** Low Power Passive Voltage Transformers, capacitive dividers will need to include a compensation due to temperature. At this point, it is important to note that once the Production ID of the LPIT is added, not only are the correction factors updated, but also the compensation values are updated.

| Temperature sensor  |           |                          |       |                |    |    |
|---------------------|-----------|--------------------------|-------|----------------|----|----|
| 1341.3331.23341.    | 138 Tem   | perature compensat.:     | on    |                | -  |    |
| 1341.3331.23341.    | 156 Loo   | p resist. intercon. phA: | 1.19  | 1.19           |    | Ω  |
| 1341.3331.23341.    | 157 Loo   | p resist. intercon. phB: | 1.21  |                |    | Ω  |
| 1341.3331.23341.    |           | p resist. intercon. phC: |       | 1.21           |    | Ω  |
| 1341.3331.23341.    |           | issipation factor 50Hz:  | 6     |                | =1 |    |
| 1341.3331.23341.    | 50 010    | issipation lactor sone.  | 2     |                | -  |    |
|                     |           |                          | T[°C] | tan delta[ppm] | -  |    |
|                     |           |                          | -40   | 3700           | ^  |    |
|                     |           |                          | -20   | 3200           | ≡  |    |
|                     |           |                          | -5    | 3100           |    |    |
|                     |           |                          | 80    | 3000           |    |    |
|                     |           |                          | 100   | 9000           | ~  |    |
| 1341.3331.23341.    | 137 C1 d  | issipation factor 1kHz:  | 6     |                |    |    |
|                     |           |                          | T[°C] | tan delta[ppm] |    |    |
|                     |           |                          | -40   | 5300           | ^  |    |
|                     |           |                          | -20   | 5200           | =  |    |
|                     |           |                          | -5    | 5300           |    |    |
|                     |           |                          | 80    | 4300           |    |    |
|                     |           |                          | 100   | 6200           | ~  |    |
| 1341.3331.23341.    | 141 Table | of temperature error:    | 20    |                |    |    |
|                     |           |                          | T[°C] | err[ppm]       |    |    |
|                     |           |                          | -40   | 16370          | ~  |    |
|                     |           |                          | -35   | 14769.999999   | =  |    |
|                     |           |                          | 0     | 4959.9999999   |    |    |
|                     |           |                          | 40    | -4799.9999999  |    |    |
|                     |           |                          | 70    | -12200         | ~  |    |
| 1341.3331.23341.    | 163       | Default temperature:     | 40    |                |    | °C |
| Cable data          |           |                          |       |                |    |    |
| 1341.3331.23341.    | 117 5     | ensor data incl. cable:  | ves   |                | -  |    |
| 10 (1.000 (1.2004). | este di   | state inter coole.       | 100   |                |    |    |

Figure 28. Temperature compensation for Capacitive Divider.

This sensor temperature will be obtained by the **7SY82** device on pins 4 and 5 of the RJ45 input as shown in Figure 17.

#### **OMICRON Test Set and Settings**

Now that was added the LPIT settings on DIGSI it is possible to reply to the data in OMICRON using the software Test Universe and all the components showed in the Figure 28.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

|                | c        | able type | Suitable for  | Connector type | Item no. |
|----------------|----------|-----------|---|----------------|----------|
|                | L        | A81       | ABB Relion  | RJ45           | B1960000 |
|                | L        | A82       | ABB REF542plus  | 2x Twin-BNC    | B1960100 |
|                |          | AB3       | ABB CSU-2   | RJ45           | P0000782 |
|                | 0000     | SE1       | Schneider Electric Sepam  | RJ45           | B1960300 |
|                |          | SE2       | Schneider Electric Easergy<br>Schweitzer Engineering Laboratories SEL-751 | 2x RJ45        | B1960500 |
| -              | L        | 511       | Siemens Siprotec 4 Compact  | RJ45           | B1960200 |
|                | L        | ST1       | Siemens 7SY821<br>Sprecher Automation SPRECON-EDIR1                       | RJ45           | P0002259 |
| OMICRON CMC430 | LLX1 BOX |           | CABLE SET   |                |          |

Figure 29. OMICRON test devices and accessories required.

First, create a New Test Document in OMICRON, then open the menu "**Device Settings**". In this menu change the values of **Vnom** and **Inom** with same values in Primary and Secondary as it is observed in the Figure 19.

As **LPIT**s covered a high primary value without a saturation, also is important to set the Limits V max and I max, in this case it is important to consider the rating voltages at the LPCT and LPVT inputs. This setting limits the maximum voltage that the test equipment would inject and therefore does not affect the inputs of the **7SY82**. For this example, Vmax will be 30% the nominal voltage and for I max, the short current of the system, for example 20 kA.

| evice Settings            |           |                                 |                 |                 |                     |                            |                |
|---------------------------|-----------|---------------------------------|-----------------|-----------------|---------------------|----------------------------|----------------|
| evice Settings            |           |                                 |                 |                 |                     |                            |                |
| Device                    |           | Nominal Values -                |                 |                 | Other Device Prope  | erties                     |                |
| Name/description:         | LPIT TEST | Number of phas                  | es: 0 2         | 3               | Drop-out time:      |                            | 20.000 m       |
| Manufacturer:             | SIEMENS   | f nom:                          | 50.000 Hz       |                 | -Limits             |                            |                |
|                           |           |                                 | Primary         | Secondary       | V max:              |                            | 43.000 kV (L-L |
| Device type:              | 75Y82     | V nom:                          | 33.000 kV (L-L) | 33.000 kV (L-L) | I max:              |                            | 20.000 k/      |
| Device address:           |           |                                 | 19.053 kV (L-N) | 19.053 kV (L-N) | -Overload Detection | n Sensitivity ———          |                |
|                           |           |                                 |                 |                 | High                | <ul> <li>Custom</li> </ul> | 50.000 m       |
| Serial/model number:      |           | I nom:                          | 1.250 kA        | 1.250 kA        | OLow                | Off                        |                |
|                           |           |                                 |                 |                 | -Debounce/Deglitch  | Filters                    |                |
|                           |           | -Residual Voltage               | and Current     |                 | Debounce time:      |                            | 3.000 m        |
| Additional information 1: |           | Direction of resivence voltage: | dual            | 3 * V0 +        | Deglitch time:      |                            | 0.000          |
| Additional information 2: |           |                                 |                 |                 |                     |                            |                |
| Substation                |           | Direction of resi               | Juai            | -3 * IO *       |                     |                            |                |
| Name:                     |           | Instrument t                    | ansformers      |                 |                     |                            |                |
| Address:                  |           |                                 | Primary         | Secondary       |                     |                            |                |
| Bay                       |           | VN:                             | 19.053 kV       | 19.053 kV       |                     |                            |                |
| Name:                     |           | VIN:                            |                 |                 |                     |                            |                |
| Address:                  | -         | IN:                             | 1,250 kA        | 1.250 kA        |                     |                            |                |
|                           | 1         |                                 |                 |                 | L                   |                            |                |

Figure 30. Device Settings in Omicron Test Universe.

In Global Hardware configuration, Menu extension devices please select LLX1.

In the menu **Amplifiers/sensor simulation/low level outputs**, please select "Add Voltage sensor", then the menu of the Figure 31 will appear.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| Analog Outputs Bin | ary / Analog Inputs Binary ( | Outputs DC Analog Inputs Tim | ne Source |             |   |           |
|--------------------|------------------------------|------------------------------|-----------|-------------|---|-----------|
| it set:            | CMC430                       | •                            | Scan      | Calibration | No extension device 🛛 🔻                               | Configure |
| est set            | Voltage systems              | Current systems              |           | Outputs     |   |           |
| nalog outputs:     | 2 -                          | 1 *                          | Configure |             | V, 25VA @ 100V, 250mArms<br>A, 96VA @ 8A, 12Vrms<br>/ |           |
| ampled Values:     | 0 -                          | 0 -                          | Configure |             | bled><br>bled><br>bled>                               |           |
| xtension devices   | Amplifiers / sensor si       | mulation / low level outputs | 1         | Outputs     |   |           |
| LX1 T              | Voltage Sensor 1             |                              | Configure |             |   |           |
|                    | <none></none>                | *                            | Configure |             |   |           |
| <none> *</none>    | <none></none>                | •                            | Configure |             |   |           |
|                    | <none></none>                | •                            | Configure |             |   |           |
| <none> *</none>    | <none></none>                | -                            | Configure |             |   |           |
|                    | <none></none>                | -                            | Configure |             |   |           |

Figure 31. Global Hardware Configuration, adding a Voltage Sensor.

At this point the user can add the **Voltage divider** ratio values showed in Figure 19. Please note that these values are Phase to Phase.

| Configure Voltage Senso | r Simulation |                        |        | ×    |
|-------------------------|--------------|------------------------|--------|------|
| Low level output:       | LL out 1-3   | Use correction factors |        |      |
| Display value (RMS):    | 10.00k V     |                        |        |      |
| Output value (RMS):     | 1.00 V       |                        |        |      |
| Residual channel        |              | OK Cancel              | Delete | Help |

Figure 32. Configure Voltage Sensor Simulation.

As the Voltage divider has correction factor, please select the option "Use correction factors" then, the following option will be showed.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| Low level output:    | LL out 1-3 🔹 | Use correction factors                                 |          |
|----------------------|--------------|--|----------|
| Display value (RMS): | 10.00 kV     | Propagation delay phase 1:                             | 0.000 µs |
| Dutput value (RMS):  | 1.00 V       | Propagation delay phase 2:                             | 0.000 µs |
|                      | 1.           | Propagation delay phase 3:                             | 0.000 µs |
|                      |              | Amplitude factor phase 1:<br>Amplitude factor phase 2: | 1.000    |
| Residual channel     |              | Amplitude factor priase 2.                             | 1.000    |
| Residual channel     |              | Amplitude factor phase 3:                              |          |
| Residual channel     |              |  | 1.000    |

Figure 33. Configure Voltage Sensor Simulation with correction factors.

The phase Error or correction angle in OMICRON Test Universe software is named "**Propagation delay phase x**:" and the values must be entered in microseconds (**not in minutes as in DIGSI**), then it is necessary to convert the values from **minutes** to **µs**, please take in count that this formula depends on the system frequency.

First, converts the angle from minutes to degrees.

Phase A correction angle:

correction angle in degrees = 
$$\frac{(\text{minutes'})}{60} = \frac{(10.67')}{60} = 0.1778^{\circ}$$

Now, following the recommendation given in [8], to convert from degrees to microseconds the following formula must be used:

propagation delay = 
$$\frac{pl}{fr \times 360^\circ} = \frac{0.1778^\circ}{50Hz \times 360^\circ} = 9.879 \,\mu s$$

Same procedure for remain phases.

Phase B: 9.685 μs Phase C: 10.028 μs

Now for this example it is necessary to add the "Amplitude factor phase x", from the SIBushing Nameplate the user can calculate this value with this formula:

Amplitude factor 
$$A = \frac{1}{CF_U} = \frac{1}{1.0.4804} = 0.9541$$

Same formula for the remaining phases:

Amplitude factor 
$$B = \frac{1}{CF_I} = \frac{1}{1.0438} = 0.9587$$
  
Amplitude factor  $C = \frac{1}{CF_I} = \frac{1}{1.03614} = 0.9651$ 

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

Once the calculation was made, the Current Sensor the data is:

| Configure Voltage Sen | sor Simulation |   | ×                       |
|-----------------------|----------------|---|-------------------------|
| Low level output:     | LL out 1-3     | Use correction factors  |                         |
| Display value (RMS):  | 10.00 kV       | Propagation delay phase 1:  | 9.879 µs                |
| Output value (RMS):   | 1.00 V         | Propagation delay phase 2:  | 9.685 µs                |
|                       | 1              | Propagation delay phase 3:  | 10.028 µs               |
| Residual channel      |                | Amplitude factor phase 1:<br>Amplitude factor phase 2:<br>Amplitude factor phase 3: | 0.954<br>0.959<br>0.965 |
|                       |                | OK Cancel   | Delete                  |

Figure 34. Data for Voltage Sensor according to SIBushing Nameplate.

Same procedure for Current Sensor, In the menu Amplifiers/sensor simulation/low level outputs, please select "Add Current sensor", then the menu of the Figure 35 will appear. Choose the correct option for Sensor Type and Signal Type.

| Configure Current Sen | sor Simulation |         |            |              |        | ×    |
|-----------------------|----------------|---------|------------|--------------|--------|------|
| Low level output:     | LL out 4-6     | •       | Use correc | tion factors |        |      |
| Display value (RMS):  | -              | 50.00 A |            |              |        |      |
| Output value (RMS):   | 22             | 2,50 mV |            |              |        |      |
| Sensor type:          | Rogowski       | -       |            |              |        |      |
| Signal type:          | Differential   | -       |            |              |        |      |
| Residual channel      |                |         |            |              |        |      |
|                       |                |         |            |              |        |      |
|                       |                |         |            |              |        |      |
|                       |                |         | ОК         | Cancel       | Delete | Help |
|                       |                |         | 1          |              |        |      |

Figure 35. Current Sensor configuration menu.

As in DIGSI, in Test Universe software the user should add the correction factor, then select the check box on the right side of the menu.

| onfigure Current Sen | sor Simulation |                            |          |
|----------------------|----------------|----------------------------|----------|
| Low level output:    | LL out 4-6     | 🗹 Use correction factors   |          |
| Display value (RMS): | 50.00 A        | Propagation delay phase 1: | 0.000 µs |
| Dutput value (RMS):  | 22.50 mV       | Propagation delay phase 2: | 0.000 µs |
|                      |                | Propagation delay phase 3: | 0.000 ps |
| Gensor type:         | Rogowski 🔻     |                            |          |
| Signal type:         | Differential * |                            |          |
|                      |                | Amplitude factor phase 1:  | 1.000    |
| Residual channel     |                | Amplitude factor phase 2:  | 1.000    |
|                      |                | Amplitude factor phase 3:  | 1.000    |

Figure 36. Current Sensor configuration menu including correction factors.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

As shown with the Voltage Divider to convert the values given in the **SIBushing** Nameplate into the magnitudes received by Omicron, please follow the next steps.

First, convert the angle from minutes to degrees.

Phase A correction angle:

correction angle in degrees = 
$$\frac{(\text{minutes'})}{60} = \frac{(-0.37')}{60} = -0.0061667^{\circ}$$

Now, following the recommendation given in [8], to convert from degrees to microseconds the following formula must be used:

propagation delay = 
$$\frac{pl}{fr \times 360^{\circ}} = \frac{-0.0061667^{\circ}}{50Hz \times 360^{\circ}} = -0.3426\mu s$$

Same procedure for remaining phases.

Phase B: -0.03703  $\mu s$  Phase C: -0.01851  $\mu s$ 

Now for this example it is necessary to add the **"Amplitude factor phase x**", from the **SIBushing** Nameplate it is possible to calculate this value with the following formula:

Amplitude factor 
$$A = \frac{1}{CF_I} = \frac{1}{1.00476} = 0.9953$$

Same formula for the remain phases:

Amplitude factor 
$$B = \frac{1}{CF_I} = \frac{1}{1.01326} = 0.9869$$
  
Amplitude factor  $C = \frac{1}{CF_I} = \frac{1}{1.02241} = 0.9781$ 

Once the calculation is done, the Current Sensor data is entered:

| Configure Current Sen | sor Simulation |                            | ×          |
|-----------------------|----------------|----------------------------|------------|
| Low level output:     | LL out 4-6     | ☑ Use correction factors   |            |
| Display value (RMS):  | 50.00 A        | Propagation delay phase 1: | -0.343 µs  |
| Output value (RMS):   | 22.50 mV       | Propagation delay phase 2: | -0.037 µs  |
|                       |                | Propagation delay phase 3: | -0.019 µs  |
| Sensor type:          | Rogowski 🔹     |                            |            |
| Signal type:          | Differential   |                            |            |
|                       |                | Amplitude factor phase 1:  | 0.995      |
| 🔲 Residual channel    |                | Amplitude factor phase 2:  | 0.987      |
|                       |                | Amplitude factor phase 3:  | 0.978      |
|                       |                | OK Cancel De               | elete Help |

Figure 37. Current sensor data registered in Test Universe software.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

Now that the hardware has been configured in OMICRON, it is possible to perform an overcurrent test in the conventional way, only it is necessary to consider that the settings in DIGSI for the protection functions are given in primary values.

In this example the test will performed according to the following settings.

| 1 |   | on                | Mode:                       | 821.1941.691.1   |
|---|---|-------------------|-----------------------------|------------------|
|   |   | no                | Operate & fit.rec. blocked: | 821.1941.691.2   |
|   |   | fundamental comp. | Method of measurement:      | 821.1941.691.8   |
|   | A | 600               | Threshold:                  | 821.1941.691.3   |
|   |   | IEC very inverse  | Type of character. curve:   | 821.1941.691.130 |
|   |   | instantaneous     | Reset:                      | 821.1941.691.131 |
|   |   | 1.10              | Time dial:                  | 821.1941.691.101 |

Figure 38. Overcurrent settings in DIGSI.

As the values defined in Test Universe are the same in Primary as in Secondary, the values in OMICRON as are follow:

| ction Para | meters                   |   |  |   |  |   |   |
|------------|--------------------------|---|--|---|--|---|---|
| ints       |                          |   |  |   |  |   |   |
| Phase      | e (1 Element / 1 Active) |   | *  |   |  |   |   |
| Active     | Element Name             | Tripping Characteristic                             | l Pick-up  | Absolute  | Time   | Reset Ratio   | Direction   |
|            | l #1 Phase               | IEC Very Inverse                                    | 1.000 Iref   | 600.0 A   | 1.100  | 0.950   | Non Directional   |
|            |                          |   |  |   |  |   |   |
|            |                          |   |  |   |  |   |   |
|            |                          |   |  |   |  |   |   |
|            | ents<br>Phas<br>Active   | Phase (1 Element / 1 Active)<br>Active Element Name | Phase (1 Element / 1 Active) Active Element Name Tripping Characteristic | Phase (1 Element / 1 Active)  Active Element Name Tripping Characteristic I Pick-up | Phase (1 Element / 1 Active)  Active Element Name Tripping Characteristic I Pick-up Absolute | ents<br>Phase (1 Element / 1 Active)  Active Element Name Tripping Characteristic I Pick-up Absolute Time | ents Phase (1 Element / 1 Active) Active Element Name Tripping Characteristic I Pick-up Absolute Time Reset Ratio |

Figure 39. Overcurrent Protection Parameters in Test Universe.

Please take into consideration the polarity of the Rogowski coil when directional test will perform.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

#### 1.5.2 Case 2. Using ABB LPIT sensors

This example considers the following Nameplate information for two different sensors (voltage and current sensors).

| ABB   |                                | Vol  | tage \$                              | Sensor       |
|---|--------------------------------|--|--------------------------------------|--------------|
| Upr: 22/√3 kV Kr<br>Fv: 1.9/8h fr:<br>24/50/125//0.82 kV                                    | 50/60 Hz                       | Cl: 0.5/3P<br>-25/80 °C<br>φο cor: -0.0<br>18 Feb 20 | φ <sub>or</sub> : 0°<br>1 kg<br>400° |              |
| ABB   |                                |  | Currer                               | nt Sensor    |
| KECA 80 C85<br>lpr: 80 A Usr:0.15<br>Kpcr: 50 fr: 50/6<br>CFI: 1.0020 φ0<br>IEC 61869-10 Ma | 0 Hz -25/80 °<br>cor: +0.0030° | C 0.72/-/-//0<br>lth/ldyn: 85                        | 5/5P630-/<br>).82 kV<br>(3s)/230 k   | A2<br>E<br>A |

Figure 40. ABB Rogowski coil KECA 80 C85 and R divider KEVA 24 C2.

In the Figure 40 the burden value of the LPIT is missing, then is necessary to check in the LPITs vendor catalogue:

- KECA 80 C85 Rated burden according to catalogue: 2 MΩ; 50 pF.
- KEVA 24 C2 Rated burden according to catalogue: 2 MΩ; 50 pF.

In this case the quantity of LPITs is 6, 3 Rogowski coils and 3 Resistive dividers, then the following picture shows the secondary connection to **7SY82**.

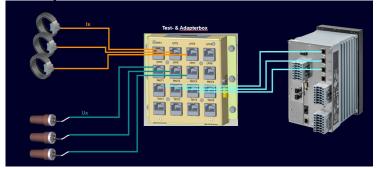


Figure 41. Connection example using an Adapter box.<sup>1</sup>

Like with **SIBushing**, the first step is to configure the **LPIT** type, including the data for adaptor test.

Please pay special attention to the units used in the Nameplate compared to the units used in DIGSI, in this case there is a difference between the angle units. To convert from degrees to minutes, the user needs to multiply by 60.

Note:

<sup>&</sup>lt;sup>1</sup> The expected Sales Release for SIEMENS Test Box Accessory is End of September 2024.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

In this example the angle correction factor is +0.0030° for a LPCT KECA 80 C85, then in minutes the value +0.18'. At the time of release this document, in DIGSI 5 the angle correction settings (*23101.108, 23101.109 & 23101.110*) can only be adjusted to negative values, however, due to the tolerance in the angle measurement of the 7SY82 (0.2° at rated current) and the low value of correction factor in this example, an approximation to 0.0' is valid.

The Figures 42 and 43 show the settings registered according to ABB Nameplate in DIGSI.

| Channel information |                           |                 |    |          |  |
|---------------------|---------------------------|-----------------|----|----------|--|
| 1341.3331.23101.100 | Input channel:            | Input 1 to 3    | ×  | <b>1</b> |  |
| endor data          |                           |                 |    |          |  |
| 1341.3331.23101.101 | Vendor of phase LPITs:    | generic         | -  |          |  |
| 1341.3331.23101.160 | Editable data view:       | Name-plate data | •  | <b>1</b> |  |
| iensor data         |                           |                 |    |          |  |
| 1341.3331.23101.103 | Rated primary current:    | 80.0            | A  |          |  |
| 1341.3331.23101.104 | Rated secondary voltage:  | 150.00          | mV | 🔁 🙈      |  |
| 1341.3331.23101.111 | Rated phase offset:       | 90.0            | •  | 🔁 🙈      |  |
| 1341.3331.23101.112 | Nominal burden:           | 2 MΩ, 50 pF     | -  | 🔁 🙈      |  |
| 1341.3331.23101.105 | Corr. factor for Kr phsA: | 1.0020          |    | 🛃 🙈      |  |
| 1341.3331.23101.106 | Corr. factor for Kr phsB: | 1.0020          |    | 🔁 🙈      |  |
|                     | Corr. factor for Kr phsC: | 1.0020          |    | 🔁 🙈      |  |
| 1341.3331.23101.107 | Corr. angle phsA:         | 0.0             | •  | 🔁 🙈      |  |
| 1341.3331.23101.107 | contangle prote           |                 |    |          |  |
|                     | Corr. angle phsB:         | 0.0             | •  | 🔁 🙈      |  |

Figure 42. ABB Rogowski coil data.

| annel information   |                           |                 |    |               |  |
|---------------------|---------------------------|-----------------|----|---------------|--|
| 1341.3331.23281.100 | Input channel:            | Input 5 to 7    | *  | <b>1</b> 2 AS |  |
| endor data          |                           |                 |    |               |  |
| 1341.3331.23281.101 | Vendor of phase LPITs:    | generic         | •  | <b>1</b>      |  |
| 1341.3331.23281.160 | Editable data view:       | Name-plate data | •  |               |  |
| Sensor data         |                           |                 |    |               |  |
| 1341.3331.23281.103 | Rated primary voltage:    | 10.0            | kV |               |  |
| 1341.3331.23281.104 | Rated secondary voltage:  | 1.00            | v  | 🔁 🔜           |  |
| 1341.3331.23281.111 | Rated phase offset:       | 0.00            | 0  | 🔁 🔊           |  |
| 1341.3331.23281.112 | Nominal burden:           | 2 MΩ, 50 pF     | •  | 🔁 🙈           |  |
| 1341.3331.23281.105 | Corr. factor for Kr phsA: | 0.9984          |    | 🛃 🔊           |  |
| 1341.3331.23281.106 | Corr. factor for Kr phsB: | 0.9984          |    | 🛃 🔊           |  |
| 1341.3331.23281.107 | Corr. factor for Kr phsC: | 0.9984          |    |               |  |
| 1341.3331.23281.108 | Corr. angle phsA:         | -2.4            |    |               |  |
| 1341.3331.23281.109 | Corr. angle phsB:         | -2.4            |    |               |  |
| 1341.3331.23281.110 | Corr. angle phsC:         | -2.4            | •  | <b>1</b>      |  |
| Cable data          |                           |                 |    |               |  |
|                     |                           |                 |    |               |  |

Figure 43. LPIT data for ABB R divider.

#### **OMICRON Test Set and Settings**

Now that was added the **LPIT** settings on DIGSI it is possible to reply to the data in OMICRON using the software Test Universe as was showed in 1.5.1.

After user creates a New Test Document in OMICRON, open the menu "**Device Settings**". In this menu change the values of **Vnom** and **Inom** with same values in Primary and Secondary as it is observed in the following picture.

As LPITs covered a high primary value without a saturation, it is important to also set the Limits V max and I max.

### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| vice Settings             |           | - Nominal Valu              |                  |                 | Other Device Presenting          |                 |
|---------------------------|-----------|-----------------------------|------------------|-----------------|----------------------------------|-----------------|
| Device                    | LPIT TEST | Nominal Valu<br>Number of p |                  | 3     3         | Other Device Properties          | 20.000 ms       |
| Manufacturer:             | SIEMENS   |                             |                  |                 |                                  | 201000 112      |
| Manufacturer:             | SIEMENS   | fnom:                       | 50.000 Hz        |                 | -Limits                          |                 |
|                           | ×         |                             | Primary          | Secondary       | V max:                           | 30.000 kV (L-L) |
| Device type:              | 75Y82     | V nom:                      | 24.000 kV (L-L)  | 24.000 kV (L-L) | I max:                           | 20.000k A       |
| Device address:           |           |                             | 13.856 kV (L-N)  | 13.856 kV (L-N) | - Overload Detection Sensitivity |                 |
|                           |           |                             |                  |                 | Igh Ocustom                      | 50.000 m        |
| Serial/model number:      |           | I nom:                      | 1.000 kA         | 1.000 kA        | O Low Off                        |                 |
|                           |           |                             |                  |                 | Debounce/Deglitch Filters        |                 |
|                           |           | -Residual Volt              | age and Current  |                 | Debounce time:                   | 3.000 m         |
| Additional information 1: |           | Direction of                | residual         | 3*V0 *          | Deglitch time:                   | 0.000           |
| Additional information 2: |           | voltage:                    |                  | 5 10            |                                  |                 |
|                           |           | Direction of<br>current:    | residual         | -3*10 *         |                                  |                 |
| Substation                |           | currenc                     |                  |                 |                                  |                 |
| Name:                     |           | Instrume                    | ent transformers |                 |                                  |                 |
| Address:                  |           |                             | Primary          | Secondary       |                                  |                 |
| Bay                       |           | VN:                         | 13.856 kV        | 13.856 kV       |                                  |                 |
| lame:                     |           |                             |                  |                 |                                  |                 |
| Address:                  |           | IN:                         | 1.000 kA         | 1.000 kA        |                                  |                 |

Figure 44. Device Settings in OMICRON Test Universe software.

In Global Hardware configuration, Menu extension devices please select LLX1.

|                 | (               |                 |   |           |  |
|-----------------|-----------------|-----------------|---|-----------|--|
| t set:          | CMC430          |                 | • | Scan      | Calibration No extension device * Configure  |
| est set         | Voltage systems | Current systems |   |           | Outputs  |
| nalog outputs:  | 2 *             | 1 .             |   | Configure | Voltage:         6x150V, 25VA @ 100V; 250mArms           Current:         3x12.5A, 96VA @ 8A, 12Vrms           Aux. DC:         115.0V |
| ampled Values:  | 0 *             | 0 *             |   | Configure | Stream 1: <disabled>       Stream 2:     <disabled>       Stream 3:     <disabled></disabled></disabled></disabled>                    |
| tension devices |                 |                 |   |           |  |
| LX1 ·           | <none></none>   |                 | • | Configure |  |
|                 | - Louis         |                 |   |           |  |
| none> *         | <none></none>   |                 | • | Configure |  |
|                 | <none></none>   |                 | • | Configure |  |
| none> *         | <none></none>   |                 | • | Configure |  |
|                 | <none></none>   |                 | • | Configure |  |
|                 |                 |                 |   |           |  |
|                 | 5:              |                 |   | Configure | Input groups: <none></none>  |

Figure 45. Hardware configuration.

In the menu **Amplifiers/sensor simulation/low level outputs**, please select "**Add Voltage sensor**" in the first option, then the follow menu will appear:

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| Extension devices | Amplifiers / sensor simulation / low level outputs |           | Outputs |
|-------------------|--|-----------|---------|
| LLX1 -            | Voltage Sensor 1                                   | Configure |         |
|                   | <none> *</none>                                    | Configure |         |
| <none></none>     | <none> *</none>                                    | Configure |         |
|                   | <none></none>                                      | Configure |         |
| <none> *</none>   | <none> *</none>                                    | Configure |         |
|                   | <none></none>                                      | Configure |         |

Now, the menu Configure Voltage Sensor Simulation will automatically appear, at this point add the Voltage Divider Ratio.

| Configure Voltage Senso | r Simulation |                        | ×           |
|-------------------------|--------------|------------------------|-------------|
| Low level output:       | LL out 1-3   | Use correction factors |             |
| Display value (RMS):    | 10.00 kV     |                        |             |
| Output value (RMS):     | 1 V          |                        |             |
| Residual channel        |              | OK Cancel              | Delete Help |

Figure 47. Voltage divider ratio.

As in DIGSI, in OMICRON the user should add the correction factor, then select the check box on the right side of the menu.

| Configure Voltage Sen | sor Simulation |  | ×          |
|-----------------------|----------------|--|------------|
| Low level output:     | LL out 1-3 🛛 👻 | Use correction factors                                 |            |
| Display value (RMS):  | 10.00k V       | Propagation delay phase 1:                             | 0.000 µs   |
| Output value (RMS):   | 1.00 V         | Propagation delay phase 2:                             | 0.000 µs   |
|                       |                | Propagation delay phase 3:                             | 0.000 µs   |
| Residual channel      |                | Amplitude factor phase 1:<br>Amplitude factor phase 2: | 1.000      |
|                       |                | Amplitude factor phase 3:                              | 1.000      |
|                       |                | OK Cancel Do   | elete Help |

Figure 48. Voltage Sensor menu with correction factor option.

Now it is necessary to convert the angle correction from degrees to  $\mu$ s, please consider that this formula depends on the frequency.

For KEVA 24 C2 the angle correction is -0.04°, then for a frequency of 50 Hz the Propagation delay is given by the following formula:

propagation delay = 
$$\frac{pl}{fr \times 360^{\circ}} = \frac{-0.04^{\circ}}{50Hz \times 360^{\circ}} = -2.22\mu s$$

Figure 46. Adding a Voltage sensor.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

Now for this example it is necessary to add the "Amplitude factor phase x", from the Nameplate of KEVA 24 C2 it is possible to calculate this value using this formula:

*Amplitude factor* 
$$= \frac{1}{CF_U} = \frac{1}{0.9984} = 1.0016$$

Once the calculation was made, the Voltage Sensor data is:

| Configure Voltage Sen | sor Simulation |   | ×                       |
|-----------------------|----------------|---|-------------------------|
| Low level output:     | LL out 1-3 💌   | Vse correction factors  |                         |
| Display value (RMS):  | 10.00 kV       | Propagation delay phase 1:  | -2,222 µs               |
| Output value (RMS):   | 1.00 V         | Propagation delay phase 2:  | -2,222 µs               |
|                       |                | Propagation delay phase 3:  | -2.222 µs               |
| 🗌 Residual channel    |                | Amplitude factor phase 1:<br>Amplitude factor phase 2:<br>Amplitude factor phase 3: | 1.002<br>1.002<br>1.002 |
|                       |                | OK Cancel Da  | elete Help              |

Figure 49. Final configuration for Voltage Sensor.

Once the Voltage Sensor has been added, the Current Sensor also needs to be added.

| Configure Current Sen | sor Simulation |                            | ×         |
|-----------------------|----------------|----------------------------|-----------|
| Low level output:     | LL out 4-6     | Use correction factors     |           |
| Display value (RMS):  | 80.00 A        | Propagation delay phase 1: | 0.000 µs  |
| Output value (RMS):   | 150.00 mV      | Propagation delay phase 2: | 0.000 µs  |
|                       |                | Propagation delay phase 3: | 0.000 μs  |
| Sensor type:          | Rogowski 🔹     |                            |           |
| Signal type:          | Differential * |                            |           |
|                       |                | Amplitude factor phase 1:  | 1.000     |
| 🔲 Residual channel    |                | Amplitude factor phase 2:  | 1.000     |
|                       |                | Amplitude factor phase 3:  | 1.000     |
|                       |                | OK Cancel De               | lete Help |



For KECA 80 the angle correction is +0.0030°, then for a frequency of 50 Hz the Propagation delay is given by the following formula:

propagation delay = 
$$\frac{pl}{fr \times 360^{\circ}} = \frac{+0.0030^{\circ}}{50Hz \times 360^{\circ}} = 0.17 \mu s$$

Now for this example it is necessary to add the "**Amplitude factor phase x**", from the Nameplate of KECA 80 it is possible to calculate this value using this formula:

Amplitude factor 
$$= \frac{1}{CF_I} = \frac{1}{1.0020} = 0.9980$$

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

Once the calculation was made, the Current Sensor data is:

| ow level output:    | LL out 4-6 🔹   | Use correction factors     |         |
|---------------------|----------------|----------------------------|---------|
| isplay value (RMS); | 80.00 A        | Propagation delay phase 1: | 0.170 μ |
| utput value (RMS):  | 150.00 mV      | Propagation delay phase 2: | 0.170 µ |
|                     |                | Propagation delay phase 3: | 0,170 µ |
| ensor type:         | Rogowski *     |                            |         |
| ignal type:         | Differential * | Amplitude factor phase 1:  | 0.998   |
| Residual channel    |                | Amplitude factor phase 2:  | 0.998   |
|                     |                | Amplitude factor phase 3:  | 0.998   |
|                     |                |                            |         |

Figure 51. Configure Current Sensor including correction factors.

Afterwards, the Protection test can now be configured and performed.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

#### 1.5.3 Case 3. Using Zelisko LPITs

In this third case, the DIGSI option to download the LPIT information database will be used, for a step-by-step please review the DIGSI manual "DIGSI5\_Onlinehelp\_en", section "Low-Power Instrument Transformer (LPIT) Universal Non-Modular Device (7SY82)".

The reference of LPITs used in this case would be the following:

- Current Measurement: LPIT Type, Iron core coil, reference SMCS/T JW1001
- Voltage Measurement: LPIT Type, R divider, reference SMVS UW1001

With the above information, it is possible to configure the IO141 module in DIGSI:

| 1                      |
|------------------------|
| LPCT: iron-core coil 💌 |
| unused 💌               |
| LPVT: R divider 🔹      |
| unused 💌               |
| Yes, with 1-m cable    |
|                        |

Figure 52. General data according to Zelisko technology.

Click on the indicated icon to download the updated database (JSON file).

| lit mode: primary | Active: settings grou | up 1 Change: settings group 1         | Compare: None |       |
|-------------------|-----------------------|---------------------------------------|---------------|-------|
| General           |                       |                                       |               |       |
| 1341.3331.20821.  | 100                   | Slot number: 1                        |               |       |
| 1341.3331.20821.  | 101 LPIT ty           | pe input 1 to 3: LPCT: iron-core coil | •             |       |
| 1341.3331.20821.  | 102 LF                | PT type input 4: unused               |               | 🔁 🔊 👘 |
| 1341.3331.20821.  | 103 LPIT tyj          | pe input 5 to 7: LPVT: R divider      |               |       |
| 1341.3331.20821.  | 104 LF                | PIT type input 8: unused              |               | 🔁 🔊 👘 |
| 1341 3331 20821   | 159 Adapt             | or and testbox: Yes, with 1-m cable   | -             |       |

Figure 53. Icon for updated database download.

Once the above icon is selected, the menu in Figure 53 will be displayed. At this point the user will have to choose the vendor and the year of manufacture as appropriate.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| Download sensor data  |   | × |
|---|---|---|
| Do you want to update the Vendors list?<br>Vendor of phase sensors: | <ul> <li>✓ Select all</li> <li>✓ SIEMENS SI-MV-GIS</li> <li>✓ SIEMENS SI-MV-AIS</li> <li>✓ ABB</li> <li>✓ Zelisko</li> <li>✓ Greenwood-Power</li> </ul> |   |
| Select manufacture year option as:<br>To year:                      | Year 🗸  |   |
|   | Download Cancel   |   |

Figure 54. Download sensor data menu.

Once the download has been successful, the following message will be displayed by **DIGSI**.

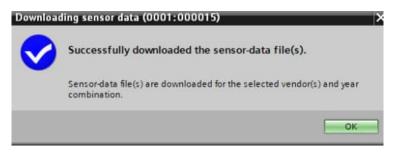


Figure 55. Successful database download.

Now that the database has been downloaded, the next step is to adjust the LPITs according to the selected vendor reference.

### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| R divider 3ph1      |                           |                |    |          |    |
|---------------------|---------------------------|----------------|----|----------|----|
| Channel information |                           |                |    |          |    |
| 1341.3331.23281.100 | Input channel:            | Input 5 to 7   |    | <b>1</b> |    |
| Vendor data         |                           |                |    |          |    |
| 1341.3331.23281.101 | Vendor of phase LPITs:    | Zelisko        | -  |          |    |
| 1341.3331.23281.162 | Sensor type:              | SMVS - UW1001  | -  | <b>1</b> | 10 |
| Sensor data         |                           |                |    |          |    |
| 1341.3331.23281.103 | Rated primary voltage:    | 24.0           | kV |          |    |
| 1341.3331.23281.104 | Rated secondary voltage:  | 3.25           | v  | 🔁 🔜      |    |
| 1341.3331.23281.111 | Rated phase offset:       | 0.00           | •  | 🔁 🔜      |    |
| 1341.3331.23281.112 | Nominal burden:           | 200 kΩ, 350 pF |    |          |    |
| 1341.3331.23281.105 | Corr. factor for Kr phsA: | 1.0000         |    |          |    |
| 1341.3331.23281.106 | Corr. factor for Kr phsB: | 1.0000         |    |          |    |
| 1341.3331.23281.107 | Corr. factor for Kr phsC: | 1.0000         |    |          |    |
| 1341.3331.23281.108 | Corr. angle phsA:         | 0.0            |    |          |    |
| 1341.3331.23281.109 | Corr. angle phsB:         | 0.0            |    |          |    |
| 1341.3331.23281.110 | Corr. angle phsC:         | 0.0            |    |          |    |

Figure 56. Voltage measurement data for LPIT.

Once selected, data such as correction factors, primary voltage, secondary voltage will be automatically recorded.

Now, same procedure for Iron Core data.

| hannel information  |                           |               |    |          |  |
|---------------------|---------------------------|---------------|----|----------|--|
| 1341.3331.23131.100 | Input channel:            | Input 1 to 3  | *  |          |  |
| /endor data         |                           |               |    |          |  |
| 1341.3331.23131.101 | Vendor of phase LPITs:    | Zelisko       | •  | <b>1</b> |  |
| 1341.3331.23131.162 | Sensor type:              | SMCS/T-JW1001 | •  | <b>R</b> |  |
| Sensor data         |                           |               |    |          |  |
| 1341.3331.23131.103 | Rated primary current:    | 80.0          | A  |          |  |
| 1341.3331.23131.104 | Rated secondary voltage:  | 150.00        | mV |          |  |
| 1341.3331.23131.111 | Rated phase offset:       | 90.00         | 0  |          |  |
| 1341.3331.23131.105 | Corr. factor for Kr phsA: | 1.0000        |    |          |  |
| 1341.3331.23131.106 | Corr. factor for Kr phsB: | 1.0000        |    |          |  |
| 1341.3331.23131.107 | Corr. factor for Kr phsC: | 1.0000        |    |          |  |
| 1341.3331.23131.108 | Corr. angle phsA:         | 0.0           |    |          |  |
| 1341.3331.23131.109 | Corr. angle phsB:         | 0.0           | *  |          |  |
|                     |                           |               |    |          |  |

Figure 57. Zelisko LPIT Iron coil data.

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

As can be seen from **Figures 56** and **57**, both the **Kr** and **angle correction** factors have a value of 1,000 and 0.0 minutes respectively, therefore, at the time of configuring the test in Test Universe software it is not necessary to choose the option of using correction factors. Now simply record the ratio values for both types of sensors.

| Configure Voltage Sensor S | imulation    |                        |        | ×    |
|----------------------------|--------------|------------------------|--------|------|
| Low level output:          | LL out 1-3 🔹 | Use correction factors |        |      |
| Display value (RMS):       | 24.00 kV     |                        |        |      |
| Output value (RMS):        | 3.25 V       |                        |        |      |
| Residual channel           |              | OK Cancel              | Delete | Help |

Figure 58. Voltage Sensor configuration.

In case of current sensor, take in count that the type of sensor is linear (Iron Core technology).

| Configure Current Sen | sor Simulation |                        | ×    |
|-----------------------|----------------|------------------------|------|
| Low level output:     | LL out 4-6     | Use correction factors |      |
| Display value (RMS):  | 80.00 A        |                        |      |
| Output value (RMS):   | 150.00 mV      |                        |      |
| Sensor type:          | Linear *       |                        |      |
| Signal type:          | Single-ended 🔹 |                        |      |
| 🗌 Residual channel    |                |                        |      |
|                       |                |                        |      |
|                       |                |                        |      |
|                       |                | OK Cancel Delete       | Help |

Figure 59. Current sensor configuration.

Now the tests can be developed following the conventional steps.

SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

### 1.6 Tipps & Tricks

#### 1.6.1 LPIT Checklist

This section provides a useful checklist to verify if the 3<sup>rd</sup> party LPIT vendor match with the **7SY82** protection device, please check all items and the Action/Conclusion described per each one. In some cases, with the answer, you can define if the solution requires an additional reviewing.

| Criteria  | Current measurement  | Voltage<br>measurement   | Action/Conclusion   |
|---|--|--|---|
| Sensor principle supported by<br><b>7SY82/ IO141</b> .  | □Rogowski coil<br>□Iron Core   | □R divider<br>□C divider   | If no option was selected, the LPIT<br>technology doesn't match with<br>7SY82 LPIT inputs.  |
| Compatibility with standard   | □IEC 61869-10  | □IEC 61869-11  | If no option was selected, please<br>contact to your Siemens sales<br>representative.   |
| "Nominal secondary value",<br>calculated using sensor transfer<br>ratio and <b>Rated peak withstand</b><br>current of application, is fixed   |  |  | If option "No" was selected, please<br>contact to your Siemens sales<br>representative.   |
| according with <b>IO141</b> module input range  | 1.6 · Vrated, LPCT<br>□Yes<br>□No  | □Yes<br>□No  |   |
| Continuous voltage rating.<br>The maximum voltage and<br>current of the circuit, e.g. short<br>circuit or overvoltage is adjusted<br>to the continuous voltage rating<br>of the 7SY82 input and LPIT ratio. |  | 10 V   | If option "No" was selected, please<br>contact to your Siemens sales<br>representative.<br>Change the LPIT for one whose<br>ratio is set to the maximum<br>continuous voltage rating. |
|   | □2 kΩ, 5000 pF<br>□20 kΩ, 500 pF<br>□200 kΩ, 350 pF<br>□2 MΩ, 350 pf<br>□2 MΩ, 50 pF<br>□10 MΩ | <ul> <li>2 kΩ, 5000 pF</li> <li>20 kΩ, 500 pF</li> <li>200 kΩ, 350 pF</li> <li>2 MΩ, 350 pf</li> <li>2 MΩ, 50 pF</li> <li>10 MΩ</li> </ul> | If no option was selected, the LPIT<br>technology data doesn't match<br>with 7SY82 LPIT inputs.   |
| Connection cable pin according<br>to standard   | □RJ45 pin 1-2  | □RJ45 pin 7-8  | If no option was selected, you<br>require to add a Test box to match<br>with the standard pin-out. Please<br>contact to your Siemens Sales<br>Representative.                         |

### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

| Cable shielding SF/FTP             | □Yes             | □Yes                   | If not, cable shielding is used, the   |
|------------------------------------|------------------|------------------------|--|
|                                    | □No              | □No                    | solution requires extra test to check<br>the accuracy, please contact to your<br>SIEMENS Sales representative. |
| Cable length < 10 m according to   | □Yes             | □Yes                   | If option "No" was selected, please  |
| 61869-13 Nr.13C.6                  | □No              | □No                    | contact the LPIT manufacturer.   |
| Data available in Nameplate        | □Ratio           | □Ratio                 | If this information is not available,  |
|                                    | □Corr. Factor Kr | □Corr. Factor Kr       | please contact the LPIT<br>manufacturer.   |
|                                    | □Corr. angle     | □Corr. angle           |  |
| For C divider, additiona           | I                | □Temperature rating    | Additional data due to technology  |
| information                        |                  | □Accuracy acc<br>Temp. | used(optional). If no additional<br>information is available, please<br>contact the LPIT manufacturer.         |
| Combisensor                        | □Yes             |                        | In case of "No" please continue to   |
|                                    | □No              |                        | the next question.   |
| lf No combi sensor, just one       | □Yes             |                        | In case of "No" your solution  |
| measurement (Voltage o<br>Current) | No               |                        | requires a Test Box accessory.<br>Please contact to your Siemens<br>Sales representative.                      |
| If LPVT and LPCT, test boy         | ∕□Yes            |                        | In case of "No" your solution  |
| accessory is used?                 | □No              |                        | requires a Test Box accessory.<br>Please contact to your Siemens<br>Sales representative.                      |
| Test box data                      | □Corr. Factor Kr |                        | If no additional information is  |
|                                    | □Corr. angle     |                        | available, please contact the Test<br>Box manufacturer.  |

#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

#### 1.6.2 OMICRON Protection Testing Library

The OMICRON Test Universe software offers a library for the development of protection tests which can be applied by the test engineer. Figure 60 shows the menu where the user can open the test library for the **75Y82**.

| Test Univ                     | verse 4.40            |                                |                         | My Favorites |
|-------------------------------|-----------------------|--------------------------------|-------------------------|--------------|
|                               |                       |                                |                         | QuickCMC     |
| *                             |                       | ×                              |                         |              |
| New Test<br>Document          | Open Test<br>Document | Test Modules<br>& Tools        | Setup<br>& Support      |              |
| Template Libraries            |                       |                                |                         |              |
| PTL - Protection Testing Libr | any                   |                                |                         |              |
|                               |                       |                                |                         |              |
|                               | Constants (C) 1000    | -2024 OMICRON electronics Gmbł | 1   License Information |              |

Figure 60. PTL – Protection Testing Library Menu.

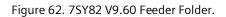
Then, the following menu will appear, click on 7SY82 V9.600 Feeder Folder

| Name                  | Date modified     | Туре        | Size |  |
|-----------------------|-------------------|-------------|------|--|
|                       | 1/11/2024 3:10 PM | File folder |      |  |
| 🚽 7SL86 V9.50 Line    | 1/11/2024 3:10 PM | File folder |      |  |
|                       | 1/11/2024 3:10 PM | File folder |      |  |
|                       | 1/11/2024 3:10 PM | File folder |      |  |
| 7SY82 V9.60 Feeder    | 4/17/2024 2:06 PM | File folder |      |  |
|                       |                   |             |      |  |
|                       |                   |             |      |  |
|                       |                   |             |      |  |
| inum C1 Ciamana Duata |                   |             |      |  |

Figure 61. Siemens Protection Testing Library Menu.

Finally, in the 7SY82 folder the user will find the files developed for testing and the user manual. Open the corresponding files and adjust them according to the solution.

| Name   | Date modified    | Туре           | Size     |
|--|------------------|----------------|----------|
| 🃸 Siemens 7SY82 V9.60 Feeder ENU TU4.31 V1.000.occ   | 3/1/2024 1:43 PM | OMICRON Contro | 4,792 KB |
| 🗃 Siemens 7SY82 V9.60 Feeder ENU TU4.31 V1.000.xrio  | 3/1/2024 1:43 PM | xrio_auto_file | 1,551 KB |
| 🛃 Siemens 7SY82 V9.60 Feeder PTT User Manual ENU.pdf | 3/1/2024 1:43 PM | PDF Document   | 371 KB   |



#### SIPROTEC 7SY82 – LPIT Technology and Secondary test injection steps

#### 1.7 References

- [1] IEC 61869-10, Instrument transformers Part 10: Additional requirements for low-power passive current transformers
- [2] IEC 61869-11, Instrument transformers Part 11: Additional requirements for low-power passive voltage transformers
- [3] Low Power Instrument Transformers for Medium Voltage Switchgear, Trench
- [4] Practical Aspects of Rogowski Coil Applications to Relaying, IEEE PSRC Special Report, September 2020
- [5] AdvaSenseTM Sensors for indoor applications Product overview, ABB.
- [6] Voltage and current sensors for intelligent transformer substations, Zelisko
- [7] IEC 62271-1, High-voltage switchgear and controlgear Part 1: Common specifications for alternating current switchgear and controlgear.
- [8] Testing ABB relays with sensor inputs, Christian-Marcel Hintea, Jakob Siemayr, Oct 14, 2022
- [9] David E. Shepard, Donald W. Yauch. An overview of Rogowski coil current sensing technology,

### 1.8 Conclusion

With the development of new technologies such as those implemented in digital substations and in this specific case the use of non-conventional measurement requires that specialists in commissioning tests, engineering, among others, be trained and have more knowledge about the test techniques associated with this technology.

This application provides support to the personnel involved in the Universal relay 7SY82 protection testing, achieving a correct conversion of units and settings required by the **DIGSI** software and **OMICRON Test Universe**.

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